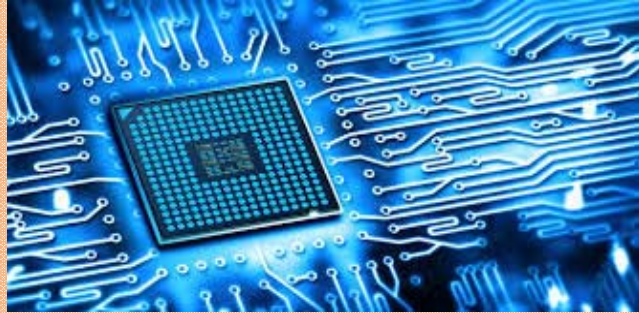


# Electronics & Communication Engineering Department

## M.Tech.

## Electronics & Communication

**25% Scholarship for Ganpat University Alumni**



**10% Scholarship for Early registration before 30<sup>th</sup> April, 2018**

- ✓ Two years full time Post Graduate Program started in 2010.
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- ✓ Experienced and qualified Faculties (Some from IISc, IITs & NITs)
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- ✓ Well equipped library with more than 35000 books and more than 700 e-journals.
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### Contact Us

**Department of Electronics & Communication Engineering**

**Ganpat University - U. V. Patel College of Engineering**

**2<sup>nd</sup> Floor, New Engineering Building, Ganpat Vidyanagar -384 012**

**02762-286805 (Extn:3251/3254) M- 96380 96944, 99097 16318**

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Programme Offered	
Course	Intake
M.Tech. (VLSI & Embedded Systems)	<b>36</b>

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- ❖ Program offered by Ganpat University in collaboration with eiTRA-eInfochips
- ❖ A world class curriculum from leading global players and eInfochips
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- ❖ State of Art labs equipped with EDA tools for Design, Verification and backend operations are in place with resourceful library
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- ❖ Application oriented syllabus
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- ❖ 24x7 access to tools
- ❖ Generation of skill matrix for performance evaluation
- ❖ **Benefit for Ganpat University Alumni (25% Scholarship)**
- ❖ **\*10% Discount for Early Bird Registration** (Last Date for Early Bird Registration: 30/04/2018)



## Contact Us

**Department of Electronics & Communication Engineering**

**Ganpat University & eInfochips Center of Excellence**

**U. V. Patel College of Engineering,**

**4, Aryan Park, Near Shilaj Rly. Crossing, Shilaj, Ahmedabad.**

**M- 99799 28955, 9909984110**